

CMP Instruction



WECON Technology Co., Ltd.

Website: <http://www.we-con.com.cn/en>

Technical Support: liux@we-con.com.cn

Skype: "fcwkkj" or "Jason.chen842"

Phone: 86-591-87868869

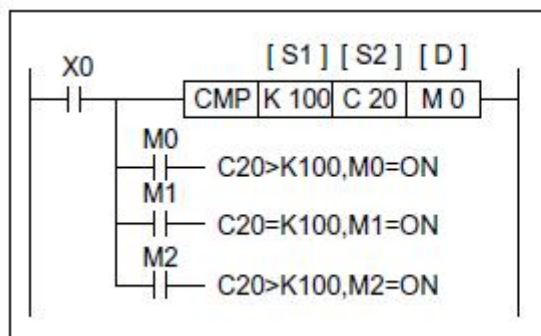
Technical forum: <http://wecon.freeforums.net/>

CMP instruction

I Instruction description:

Mnemonic	Function	Operands			Program steps
		S1	S2	D	
CMP (Compare)	Compares two data values - results of <, = and > are given.	K, H, KnX, KnY, KnM, KnS, T, C, D, V, Z	Y, M, S Note: 3 consecutive devices are used.		CMP, CMPP: 7 steps DCMP, DCMP: 13 steps

II Operation



Operation:

The data of S1 is compared to the data of S2. The result is indicated by 3 bit devices specified from the head address entered as D. The bit devices indicate:

S2 is less than S1 - bit device D is ON

S2 is equal to S1 - bit device D+1 is ON

S2 is greater than S1 - bit device D+2 is ON

Note: The destination (D) device statuses will be kept even if the CMP instruction is deactivated. Full algebraic comparisons are used, i.e. -10 is smaller than +2 etc.

III Program

