

# WECON

# Programming



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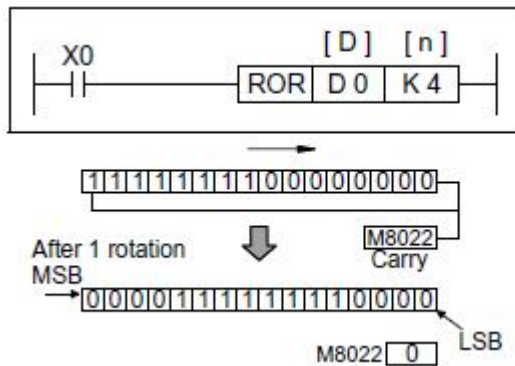
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# ROR Instructions

## 1. Instruction Description

Mnemonic	Function	Operands		Program steps
		D	n	
ROR FNC 30 (Rotation right) ➔	The bit pattern of the destination device is rotated 'n' places to the right on every execution	KnY, KnM, KnS, T, C, D, V, Z Note: 16 bit operation Kn=K4, 32 bit operation Kn=K8	K, H, ☒  Note: 16 bit operation n ≤ 16 32 bit operation n ≤ 32	ROR, RORP: 5 steps  DROR, DRORP: 9 steps

## 2.Operation:



## Operation:

The bit pattern of the destination device (D) is rotated n bit places to the right on every operation of the instruction.

The status of the last bit rotated is copied to the carry flag M8022.

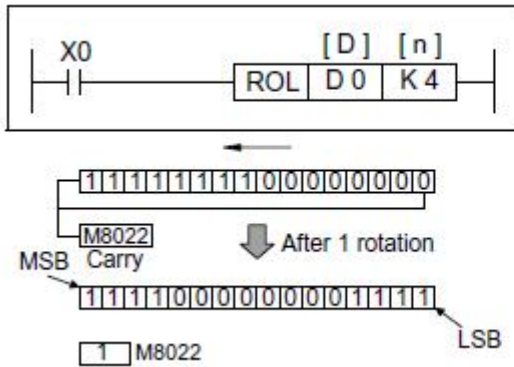
The example shown left is based on the instruction noted above it, where the bit pattern represents the contents of D0.

# ROL Instructions

## 1. Instruction Description

Mnemonic	Function	Operands		Program steps
		S	D	
ROL FNC 31 (Rotation left) ➔	The bit pattern of the destination device is rotated 'n' places to the left on every execution	KnY, KnM, KnS, T, C, D, V, Z Note: 16 bit operation Kn= K4, 32 bit operation Kn= K8	K, H, ☒  Note: 16 bit operation n ≤ 16 32 bit operation n ≤ 32	ROL, ROLP: 5 steps  DROL, DROLP: 7 steps

## 2.Operation:



### Operation:

The bit pattern of the destination device (D) is rotated n bit places to the left on every operation of the instruction.

The status of the last bit rotated is copied to the carry flag M8022.

The example shown left is based on the instruction noted above it, where the bit pattern represents the contents of D0.

### 3.Demo instruction:

