

WECON

Programming



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ALT Instructions

1. Instruction Description

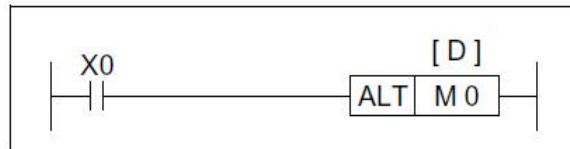
Name	Function	Bits(bits)	Pulse type	Instruction format	Step
ALT	Output	16	No	ATL (D)	3
ALTP	alternatively	16	Yes		3

This instruction reverses (D) component state when the power flow is effective. (D) is bit variable component. Usually,

(D) is bit variable component. Usually, the pulse operation type is preferred.

Operand	Bit component				Word component											
	X	Y	M	S	K	H	KnX	KnY	KnM	KnS	T	C	D	V	Z	
(D)		✓	✓	✓												

Operation:



2.Operation:

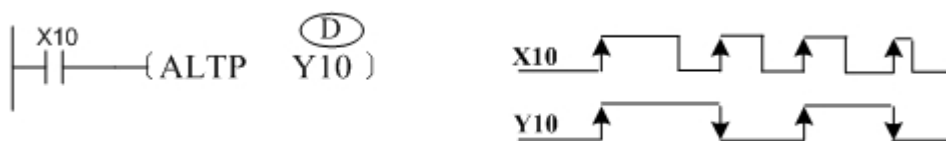
The status of the destination device (D) is alternated on every operation of the ALT instruction.

This means the status of each bit device will flipflop between ON and OFF. This will occur on every program scan unless a pulse modifier or a program interlock is used.

The ALT instruction is ideal for switching between two modes of operation e.g. start and stop, on and off etc.

3.Programming example

Example 1 for instruction:

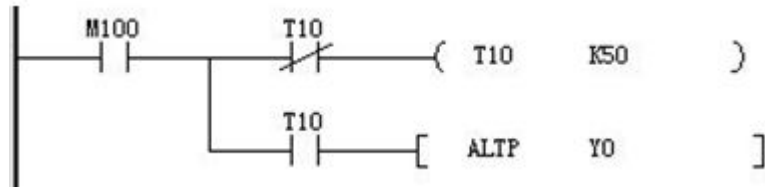


The following instruction operation is the same:



Example 2 for instruction:

If the timer is introduced in the instruction power flow, it is easy to implement oscillator output (the function can also be implemented by using a special timer STMR instruction), which is shown in the following figure:



4. PLC monitor

